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 Semiconductor Manufacturing, 2000. Proceedings of ISSM 2000. The Ninth
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2 A new auto-lock circuit concept guaranteeing low jitter in PLL frequency synthesizers irrespective of process variations

O'Sullivan, E.; Kawaguchi, M.; Shimoda, A.;
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 Iijima, N.; Ueda, Y.; Fujisaka, T.; Kondo, M.; Mizuno, M.;
 Radar Conference, 1995., Record of the IEEE 1995 International, 8-11 May 1995

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